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FROM: Peter J. Yim
Reg. 44,417**DATE:** October 27, 2005

Number of pages with cover page:	9	
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Comments:**PLEASE PROCESS THE ATTACHED.**

U.S. Patent Application Serial No. 10/799,061
For: SELECTING DIE PLACEMENT ON A SEMICONDUCTOR
WAFER TO REDUCE TEST TIME
By: Eitan CADOURI
Our Reference: 52432-20012.00

Attached is the following:

1. Transmittal (1 page)
2. Amendment Under 37 CFR 1.111 (7 pages)

OCT 27 2005

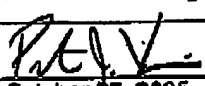
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application Number	10/799,061
		Filing Date	March 12, 2004
		First Named Inventor	Eitan CADOURI
		Group Art Unit	2829
		Examiner Name	J. Hollington
Total Number of Pages in This Submission	8	Attorney Docket Number	624322001200

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment/Reply (7 pages) <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below) Facsimile Cover Sheet (not counted as part of this submission)
Remarks		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual Name	MORRISON & FOERSTER LLP (Customer Number 20872) Peter J. Yim - Reg. No. 44,417	
Signature		
Date	October 27, 2005	

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Dated: October 27, 2005

Signature: Valerie Cohen (Valerie Cohen)

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Docket No.: 524322001200
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Eitan CADOURI

Application No.: 10/799,061

Confirmation No.: 6747

Filed: March 12, 2004

Art Unit: 2829

For: SELECTING DIE PLACEMENT ON A
SEMICONDUCTOR WAFER TO REDUCE
TEST TIME

Examiner: J. M. Hollington

AMENDMENT UNDER 37 C.F.R. 1.111

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the non-final Office Action dated July 27, 2005 (Paper No. 20050722), for which a response is due on October 27, 2005. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

sf-1980100